

QUALIFICATION PLAN			
TEST	CONDITIONS	SAMPLE SIZE	EXPECTED COMPLETION DATE
High Temperature Operating Life (HTOL)*	JEDEC <i>JESD22-A108</i>	3x32	Complete
Highly Accelerated Stress Test (HAST)*	JEDEC <i>JESD22-A110</i>	3x32	Complete
Temperature Cycle (TC)*	JEDEC <i>JESD22-A104</i>	3x32	Complete
ELF	MIL-STD-883, Method 1015	3x667	Complete
Autoclave (AC)*	JEDEC <i>JESD22-A102</i>	3x32	Complete
High Temperature Storage Life (HTSL)	JEDEC <i>JESD22-A103</i>	1x32	Complete
Solder Heat Resistance (SHR)*	<i>ADI-0049</i>	3x11	Complete
Latch-Up	JEDEC <i>JESD78</i>	6	Complete
Electrostatic Discharge <i>Human Body Model</i>	ESDA/JEDEC <i>JDS-001-2011</i>	3/voltage	Complete
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	JEDEC <i>JESD22-C101</i>	3/voltage	Complete

*Preconditioned per JEDEC/IPC J-STD-020